



EV077335093US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#10/c
4a
8/28/02

Application Serial No. 09/909,111
Filing Date July 18, 2001
Inventor Mark Fischer, et al.
Assignee Micron Technology, Inc.
Group Art Unit 1765
Examiner Duy Deo
Attorney's Docket No. MI22-1777
Title: Semiconductor Processing Methods of Forming a Conductive Projection and
Methods of Increasing Alignment Tolerances

**PRELIMINARY AMENDMENT RESPONSE TO JUNE 14, 2002 OFFICE ACTION
TO ACCOMPANY RCE FILING**

To: Assistant Commissioner for Patents
Attn: Art Unit 1765
Washington, D.C. 20231

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)
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Response to the Office Action dated **June 14, 2002**, Applicant amends and
remarks as follows:

RECEIVED
AUG 28 2002
TC 1700

Adjustment date: 10/05/2002 ECKUARY
08/27/2002 LABRAHA1 00000042 03309111
01 AC:117 -920.00 DP

AMENDMENTS

In the Claims

Please replace the claims with the following clean version of the entire set
of pending claims, in accordance with 37 CFR § 1.121(c)(1)(i). Cancel all
previous versions of any pending claim.

A marked-up version showing amendments to any claims being changed
in provided in one or more accompanying pages separate from this amendment

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